

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(See many sheets as necessary)

Complete if Known

Application Number	10/606,812
Filing Date	June 26, 2003
First Named Inventor	Subramanian, Kanakasabapathi
Group Art Unit	2812
Examiner Name	Unknown

Attorney Docket No: 1153.072US1



**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
--------------------	---------------------	------------------	---	-------	----------	----------------------------

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
---------------------	---------------------	------------------	---	-------	----------	----------------

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

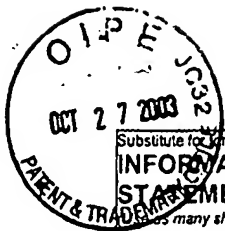
Examiner Initials *	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		AYON, A. A., et al., "Characterization of a time multiplexed inductively coupled plasma etcher", <u>Journal of the Electrochemical Society</u> , 146(1), (January 1999), 339-349	
		CHEN, JUN, et al., "Field emission from crystalline copper sulphide nanowire arrays", <u>Applied Physics Letters</u> , 80 (19), (May 2002), 3620-3622	
		CUI, YI, et al., "Nanowire nanosensors for highly sensitive and selective detection of biological and chemical species", <u>Science</u> , 293 (5533), (August 2001), 1289-1292	
		HUBER, T. E., et al., "Thermopower of Bi nanowire array composites", <u>Thermoelectric Materials 2000. Materials for Small-Scale Refrigeration and Power Generation Applications</u> , 626, Materials Research Society Symposium Proceedings, San Francisco, CA, (April 2000), Z14.2.1-Z14.2.5	
		JU, X., et al., "Preparation and photoluminescence of nanowire array and films of cadmium sulfide by electrodeposition in organic solvent", <u>Proceedings of the SPIE</u> , 4601, (2001), 260-265	
		LI, JIAN-LONG, "Spontaneous formation of ordered indium nanowire array on Si(001)", <u>Applied Physics Letters</u> , 79(17), (October 2001), 2826-2828	
		LIU, H. I., et al., "Self-limiting oxidation for fabricating sub-5nm silicon nanowires", <u>Applied Physics Letters</u> , 64(11), (March 1994), 183-1385	
		LIZHU, ZHANG, et al., "Photoluminescence of a silicon quantum wire array fabricated by electrolytic etching", <u>Chinese Physics</u> , 12(3), (July-Sept. 1992), 768-772	
		MILANOVIC, V., et al., "Micromachining Technology for Lateral Field Emission Devices", <u>IEEE Transactions on Electron Devices</u> , 48(1), (January 2001), 166-173	
		NAKAYAMA, T., et al., "Thermally stable C60 nanowires on Si substrates", <u>RIKEN Review</u> , No. 45, Inst. Phys. Chem. Res., (March 2002), 3-5	
		ONO, T., et al., "Micromachined probe for high density data storage", <u>Technical Digest. CLEO/Pacific Rim 2001. 4th Pacific Rim Conference on Lasers and Electro-Optics</u> , pt. 2, p II, Chiba, Japan Piscataway, NJ: IEEE, 2001, (July 15-19, 2001), 542-543	
		POTTS, A., et al., "Fabrication of free-standing single-crystal silicon nanostructures for the study of thermal transport and defect scattering in low	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 506. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. \* Applicant's unique citation designation number (optional) \* Applicant is to place a check mark here if English language Translation is attached



Substitute for Form 1449A/PTO  
INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT  
(Use as many sheets as necessary)

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

PTO/SSO&A(10-01)  
Approved for use through 10/1/2002. OMB 551-003  
US Patent & Trademark Office, U.S. DEPARTMENT OF COMMERCE


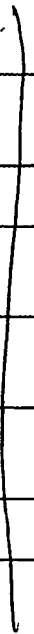

Complete if Known

Application Number	10/606,812
Filing Date	June 26, 2003
First Named Inventor	Subramanian, Kanakasabapathi
Group Art Unit	2812
Examiner Name	Unknown

Sheet 2 of 2

Attorney Docket No: 1153.072US1

### OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CONTINUED		dimensional systems", <u>Japanese Journal of Applied Physics, Part 1 (Regular Papers &amp; Short Notes)</u> , 29(11), (November 1990), 2675-2679	
		ROY, E., "Electrochemical synthesis of antimony nanowires and analysis of diffusion layers", <u>Journal of Nanoscience and Nanotechnology</u> , 1 (3), (Sept. 2001), 323-329	
		WANG, Z., et al., "AFM study of gold nanowire array electrodeposited within anodic aluminum oxide template", <u>Applied Physics A (Materials Science Processing)</u> , A74 (4), Springer-Verlag, (April 2002), 563-565	
		WANG, Y. W., et al., "Fabrication and magnetic properties of highly ordered Co16Ag84 alloy nanowire array", <u>Applied Physics A (Materials Science Processing)</u> , A74 (4), Springer-Verlag, (April 2002), 577-580	
		WANG, Y. Q., et al., "One-dimensional growth mechanism of amorphous boron nanowires", <u>Chemical Physics Letters</u> , 359 (3-4), (June 2002), 273-277	
		WANG, J. Z., et al., "Ordered Ga wires formed on Si(100)-2*n: Scanning tunneling microscopy study", <u>Physical Review B, Condensed Matter and Materials Physics</u> , 65 (23), (June 2002), 235303/1-5	
		WESTWATER, J., et al., "Growth of silicon nanowires via gold/silane vapor-liquid-solid reaction", <u>Journal of Vacuum Science and Technology</u> , 15(3), (May 1997), 554-557	
		ZAIDI, S. H., et al., "Multiple nanowire gate field effect transistors", <u>Proceedings of the 2001 1st IEEE Conference on Nanotechnology, IEEE-NANO 2001, IEEE-NANO, Maui, HI, (October 2001)</u> , 189-194	
		ZHANG, Y. F., et al., "Bulk-quantity Si nanowires synthesized by SiO sublimation", <u>Journal of Crystal Growth</u> , 212, (April 2000), 115-118	
		ZHANG, J., et al., "Morphology and Raman scattering spectrum of GaN nanowires embedded in nanochannels of template", <u>Journal of Physics D, Applied Physics</u> , 35 (13), (July 2002), 1481-1485	
		ZHU, HAO, "Fabrication and magnetic properties of Fe14Ni86 alloy nanowire array", <u>Journal of Physics: Condensed Matter</u> , 13 (8), (February 2001), 1727-1731	

EXAMINER

DATE CONSIDERED

3/30/05

Substitute Disclosure Statement Form (PTO-1448)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional) Applicant is to place a check mark here if English language Translation is attached

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)	Complete if Known	
	Application Number	10/606,812
	Filing Date	June 26, 2003
	First Named Inventor	Subramanian, Kanakasabapathi
	Group Art Unit	2812
	Examiner Name	Unknown
Sheet 1 of 1	Attorney Docket No: 1153.072US1	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS						
Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		BOURLAND, S. , et al., "Silicon-on-insulator Processes for the Fabrication of Novel Nanostructures", J. Vac. Sci. Technol. B, 19, No. 5, (Sept. 2001), 1995-1997	
		DOHERTY, L. , et al., "Application of MEMS technologies to nanodevices", Proceedings of the 2003 IEEE International Symposium on Circuits and Systems, Vol. 3, (2003), 934-937	
DUPLICATE		MILANOVIC, V. , et al., "Micromachining Technology for Lateral Filed Emission Devices", IEEE Transactions on Electron Devices, Vol. 48(1), (Jan. 2001), 166-173	
		NAMATSU, H. , et al., "Fabrication of One-Dimensional Nanowire Structures Utilizing Crystallographic Orientation in silicon and their Conductance Characteristics", J. Vac. Sci. Technol. B, 15, No. 5, (Sept. 1997), 1688-1696	

EXAMINER

DATE CONSIDERED

3/30/05

Substitute Disclosure Statement Form (PFD-1442)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached